

Notice of References Cited			Application No. 09/212,726	Applicant(s) Schuegraf		
			Examiner • Erik Kielin	Group Art Unit 2813	Page 1 of 1	
U.S. PATENT DOCUMENTS						
	DOCUMENT NO.	DATE	NAME		CLASS	SUBCLASS
A	5,710,079	5/1996	Sukharev*		438	778
B			:			
C						
D						
E						
F						
G						
H						
I						
J						
K						
L						
M						
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
N						
O						
P						
Q						
R						
S						
T						
NON-PATENT DOCUMENTS						
	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
X u	S. Wolf, Silicon Processing for the VLSI Era, Vol. 1, Sunset Beach: Lattice Press, pp 166-171.					1986
v						
w	:					
x						